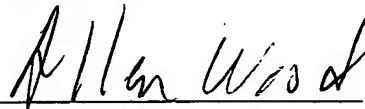## REMARKS

The present Amendment is being filed to improve the grammar and consistency of claim 1. The improvement in grammar has been achieved by adding the word "and," and the improvement in consistency will be discussed below.

Claim 1 recites first and second RAMs (see RAMs 11 and 12 in Figure 1 of the application's drawings), first and second registers (see registers 13 and 14 in Figure 1), a cumulative arithmetic unit (see cumulative arithmetic unit 18 in Figure 1), and "a selector that alternately outputs the data outputted from the first register and the second register to the cumulative arithmetic unit" (see selector 17 in Figure 1). The present Amendment After Allowance seeks to change the "cumulative arithmetic unit" recitation of claim 1, as follows (using brackets and underlining to show the proposed change): "a cumulative arithmetic unit that computes the data outputted from the first [[RAM]] register and the second [[RAM]] register ." Although the cumulative arithmetic unit operates on data from the first and second RAMs, the data is received by the cumulative arithmetic unit by way of the first and second registers. Since claim 1 provides that the recited selector "alternately outputs data outputted from the first register and the second register to the cumulative arithmetic unit," it is believed that consistency will be promoted (and possible source of confusion eliminated) by revising claim 1 as proposed.

Since the claim revisions presented herein will improve the issued patent, it is respectfully requested that the present Amendment After Allowance be entered.

Respectfully submitted,

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